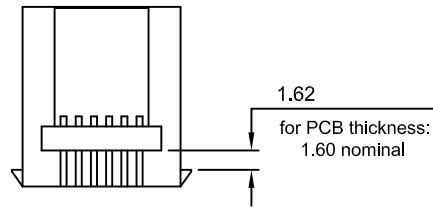
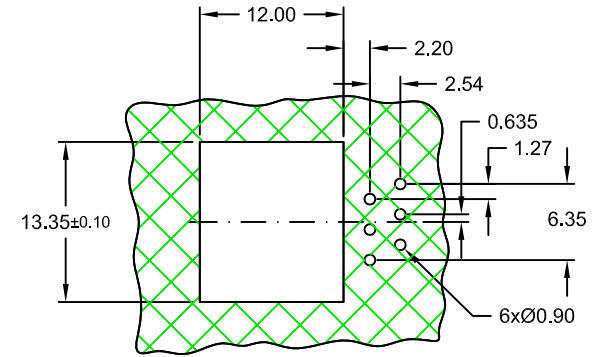
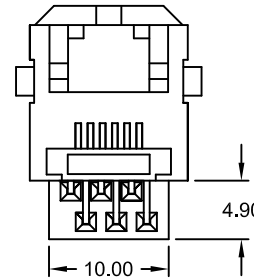
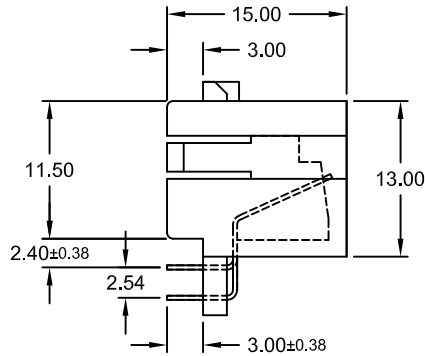
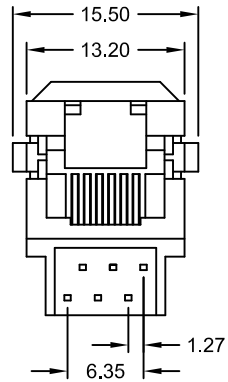


REV.	DESCRIPTION	DATE	DRAWN
A	New	12.12.2009	Ronny
B	New DWG. No.	01.02.2012	Ronny
C	correct Layout DIM 0.35 -> 0.635	12.04.2013	Ronny
D	P/N -H130- -> -S1C0- & 3D	17.03.2016	Ronny

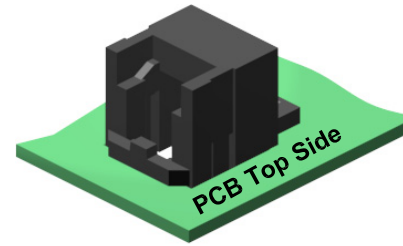
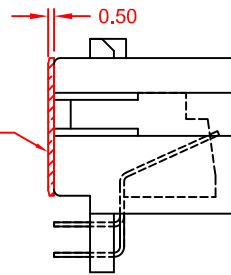
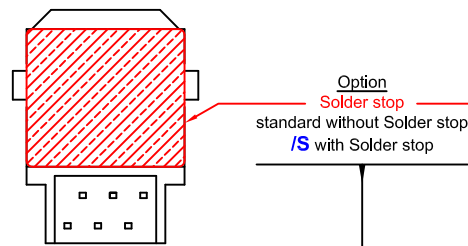


Bottom view without Solder stop



Recommended PCB Layout Top View  
(Tolerance ±0.05)

Bottom view with Solder stop



**Specifications**

**Electrical**

Current rating: 1.5A max. (@25°C)  
 Insulation resistance: 500MQ min.  
 Voltage rating: 150V AC max.  
 Withstanding voltage: 1000V rms / Minute (60Hz)  
 Contact resistance: 20mΩ max.

**Material**

Contact: Phosphor Bronze t=0.35mm  
 Plating: Sn/Au over Ni  
 Insulator: PBT (black) UL 94V-0

Operating temperature: -40°C to +85°C

Cavity comply with FCC Rules and Regulations Part 68, Subpart F

Modular Jack delivered with Plastic solder stop installed, to prevent solder entering the Jack.  
 Remove solder stop after soldering.

PART NUMBER	GOLD PLATING CONTACT AREA
MJB-166-S1C0-96/S	6µ"
MJB-166-S1C0-97/S	15µ"
MJB-166-S1C0-98/S	30µ"
MJB-166-S1C0-9H/S	50µ"

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UNIT	GENERAL TOLERANCE		DRAWN	DATE	DWG. NO.	SHEET 1/1
mm	X.° ±	X. ±	Ronny	12.12.2009	1211300	REV. D
SCALE Free	.X° ±	.X ±	Hogi	17.03.2016	SERIES NO.	
	.XX° ±	.XX ± 0.25	APPROVE Hogi	17.03.2016	MJB-166-S1C0-9x/X	
	.XXX° ±	.XXX ±				

**RJ12 PCB JACK <6P6C>**  
**Bottom entry Type**  
**non shielded**